



(12) **EUROPEAN PATENT APPLICATION**

(43) Date of publication:
26.04.2000 Bulletin 2000/17

(51) Int Cl.7: **H05K 3/02, H05K 3/38**

(21) Application number: **99308239.5**

(22) Date of filing: **19.10.1999**

(84) Designated Contracting States:
AT BE CH CY DE DK ES FI FR GB GR IE IT LI LU
MC NL PT SE
 Designated Extension States:
AL LT LV MK RO SI

(30) Priority: **19.10.1998 JP 29739998**
21.10.1998 JP 31841998

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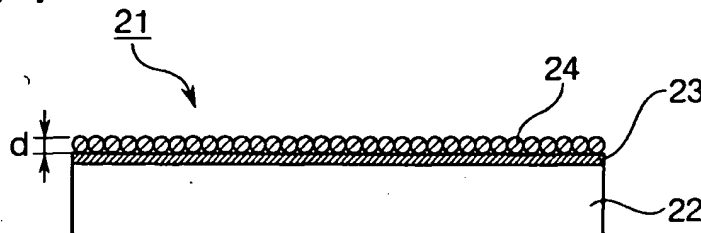
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(54) **Novel composite foil, process for producing the same and copper-clad laminate**

(57) A composite foil comprising a conductive carrier, an organic release layer coating a surface of the conductive carrier, and a conductive particle layer formed on the organic release layer. A process for producing a composite foil, comprising the steps of forming an organic release layer on a surface of a conductive carrier, and electrodepositing a conductive particle layer on the organic release layer. A copper-clad laminate comprising the above composite foil laminated to a base mate-

rial. A copper-clad laminate comprising the above composite foil laminated to a base material, having only the conductive carrier removed therefrom. Thus, there are provided a composite foil for printed wiring board production from which a copper-clad laminate enabling laser perforating without forming burrs even at lower laser power and further enabling forming fine-pitch wiring patterns can be produced, a process for producing the composite foil and a copper-clad laminate produced therefrom.

Fig. 1



Description

[0001] The present invention relates generally to a composite foil for use in the production of a printed wiring board and to a process for producing the composite foil. More particularly, the invention relates to a composite foil for printed wiring board production, with which the laser perforating of a copper-clad laminate produced from the composite foil can be carried out without forming burrs even at lower laser power. Meantime, the copper-clad laminate makes possible a fine-pitch printed wiring board.

[0002] Recently, the pattern widths and spacings of printed wiring (i.e. circuit) boards have been densified year by year in accordance with the down sizing of electronic equipment. Accordingly, the thickness of metal foils for such application should be decreased from 18 μm or 12 μm to 9 μm .

[0003] Reduction of the foil thickness is necessary, and trials of producing ultra-thin copper foils have been made in order to cope with the demand. However, copper foils with a thickness of 9 μm or less are likely to wrinkle or tear in processing, and fully satisfactory ultra-thin copper foils have not been obtained.

[0004] Further, the ultra-thin copper foil, when employed as an outer layer of a multilayer printed wiring board, encounters problems of breakage (tear) or wrinkling attributed to the deformation of the copper foil, caused by the inner wiring patterns during hot pressing, so that a peelable carrier capable of preventing the deformation of the copper foil is inevitably required.

[0005] A copper foil with the peelable carrier is generally furnished with a release layer which is placed between the copper foil and the carrier, because only the carrier is stripped off after the lamination. For example, when a copper carrier is employed, chromate as a release layer has been proposed (see, for example, Japanese Patent Application Publication (Examined) No. Sho 53-18329).

[0006] When an aluminum carrier is employed, some release layers shown below have been proposed.

A layer composed of a sulfide or oxide of Cr, Pb, Ni or Ag;

A layer of Ni or Ni alloy plating formed after zinc substitution; or

A layer of aluminum oxide coating formed again after the removal of surface oxides (see Japanese Patent Application Publication (Examined) No. Sho 60-31915).

[0007] Still further, the inventors have proposed a composite foil comprising a metal carrier and an ultra-thin copper foil of 20 μm or less thickness having an organic release layer interposed therebetween.

[0008] However, when the copper-clad laminate produced from the above conventional composite foils is perforated to form via holes, burrs might be found at edges of the perforated copper foil. Plating growth at the burrs is faster than at the flat portions of the copper foil, so that the burrs are enlarged to thereby cause problems of poor adhesion with an etching resist. Accordingly, it is preferred to remove the copper foil at the portions to be perforated by, for example, etching in advance. However, this means that an additional step is required in the perforation. Moreover, when the copper foil is ultra-thin, the removal of burrs by etching has caused undesirable thinning of the copper foil itself.

[0009] In these circumstances, the inventors have conducted extensive and intensive studies. As a result, it has been found that the formation of a copper-clad laminate by providing a composite foil comprising a conductive particle layer deposited on an organic release layer coating a carrier, for example, a copper foil or copper alloy foil, laminating a substrate with this composite foil and stripping off the carrier enables forming an upper-layer wiring pattern from the conductive particle layer to thereby solve the problem of burr occurrence, etc. at the perforating of the copper-clad laminate. Furthermore, it has been found that the conductive particle layer has excellent adhesion with resin. The present invention has been completed on the basis of these findings.

[0010] The present invention has been made with a view toward solving the above problems of the prior art. Accordingly, an object of the present invention is to provide a composite foil for printed wiring board production, with which the laser perforating of a copper-clad laminate produced from the composite foil can be carried out without forming burrs even at lower laser power. Meantime the copper-clad laminate makes possible fine pitch wiring patterns. Another object is to provide a process for producing the composite foil for printed wiring board production.

[0011] The composite foil of the present invention may comprise:

a conductive carrier,

an organic release layer coating a surface of the carrier, and

a conductive particle layer formed on the organic release layer.

[0012] The conductive particle layer preferably has a thickness ranging from 0.1 to 5.0 μm .

[0013] Further, the conductive particle layer is preferably composed of copper or a copper alloy.

[0014] It is preferred that the carrier be a copper foil or a copper alloy foil.

[0015] The organic release layer is preferably composed of at least one organic compound selected from the group

consisting of nitrogen-containing compounds, sulfur-containing compounds and carboxylic acids.

[0016] It is preferred that the conductive particle layer be composed of fringed, nodular or whiskery conductive fine particles.

[0017] The process for producing a composite foil according to the present invention comprises the steps of:

forming an organic release layer on a carrier surface, and
electrodepositing a conductive particle layer on the organic release layer with the use of a plating bath through which current is passed at a density of 10 to 50 A/dm².

[0018] The carrier is preferably a copper foil or a copper alloy foil. The organic release layer is preferably composed of at least one organic compound selected from the group consisting of nitrogen-containing compounds, sulfur-containing compounds and carboxylic acids.

[0019] It is preferred that the conductive particle layer be composed of particles having a shape of fringed, nodular or whiskery.

[0020] The copper-clad laminate of the present invention comprises the above composite foil laminated with a base material.

[0021] Further, the copper-clad laminate of the present invention may comprise the above composite foil laminated with a base material, having only the carrier removed therefrom.

[0022] In the accompanying drawings:

Fig. 1 shows a schematic sectional view of an embodiment of the composite foil of the present invention;

Fig. 2 shows a panel plating process for producing a printed wiring board, in which an embodiment of the composite foil of the present invention is used;

Fig. 3 shows a pattern plating process for producing a printed wiring board, in which an embodiment of the composite foil of the present invention is used.

[0023] Hereinafter, the novel composite foil of the present invention and the process for preparing the same will be described in detail.

[0024] Fig. 1 shows a schematic sectional view of an embodiment of the novel composite foil according to the present invention. In the embodiment, the composite foil 21 comprises a carrier 22, an organic release layer 23 superimposed on a surface of the carrier, and conductive particle layer 24 peelably formed on surface of the organic release layer.

[0025] The conductive carrier is not limited, for example, in its material, thickness, etc., when it is conductive and can form thereon the conductive particle layer. Such carrier 22 can be selected from various materials and preferably a metal foil including a copper foil, a copper alloy foil, an aluminum foil, a copper-plated aluminum foil, a zinc-plated aluminum foil and a stainless steel foil. Of these, the copper foil and the copper alloy foil are especially preferred.

[0026] The conductive carrier may be passivated with, for example, zinc and chromate.

[0027] It is preferred that the thickness of the conductive carrier 22 be in the range of 5 to 200 μm , preferably 12 to 110 μm , and especially 18 to 70 μm .

[0028] Although the organic release layer 23 is not particularly limited as long as it has release capability, it is preferred that the organic release layer 23 be composed of at least one organic compound selected from the group consisting of nitrogen-containing compounds, sulfur-containing compounds and carboxylic acids.

[0029] Examples of the nitrogen-containing compounds include triazole having a substituent (functional group), such as 1,2,3-benzotriazole, carboxybenzotriazole, N,N'-bis(benzotriazolylmethyl)urea and 3-amino-1H-1,2,4-triazole. Examples of the sulfur-containing compounds include mercaptobenzothiazole, thiocyanuric acid and 2-benzimidazolthiol.

[0030] Examples of carboxylic acids include monocarboxylic acids such as oleic acid, linolic acid and linolenic acid.

[0031] It is generally preferred that the thickness of the organic release layer 23 be in the range of 10 to 1000 Å, especially 20 to 500 Å.

[0032] Conductive fine particles constituting the conductive particle layer 24 deposited on the surface of the organic release layer 23 are not particularly limited as long as they are conductive. Examples thereof include:

metals such as copper, silver, gold, platinum, zinc and nickel and alloys of the metals;
inorganic compounds such as iridium oxide and tin oxide; and
organic compounds such as polyaniline.

[0033] It is preferred that the conductive fine particles be composed of copper or a copper alloy among the above.

[0034] The conductive particle layer 24 may be composed of conductive fine particles of a single species selected from among the above, or fine particles of a mixture of at least two species of conductive substances.

[0035] The thickness (d), dimension in the direction of the thickness of the composite foil, of the conductive particle

layer 24 is preferably in the range of 0.1 to 5.0 μm , still preferably 0.2 to 3.0 μm , yet still preferably 0.2 to 2.0 μm , and optimally 0.5 to 1.0 μm .

[0036] The thickness (d) can be measured by using a micrometer or a sectional view of the composite foil by optical microscope and/or SEM (Scanning Electron Microscope).

[0037] Although the configuration of the conductive particle layer 24 is not particularly limited, it is preferred that the conductive particles constituting the conductive particle layer 24 be fringed, nodular or whiskery. For example, the conductive particle layer 24 is preferably composed of a fringed aggregate of primary particles with a size of about 0.1 to 3.0 μm .

[0038] The conductive particle layer 24 is preferably formed on the organic release layer at a density of 2 to 10 g/m², still preferably 4 to 8 g/m². It is preferred that least some of conductive fine particles are not joined to the other conductive fine particles. Thus, the conductive particle layer 24 may have conductive fine particles, or aggregates of conductive fine particles separated from each other. Thus, the conductive particle layer is not like a film or foil which can be separated as a continuous layer.

[0039] In the present invention, the conductive particle layer 24 may have its surface plated with a covering layer. When the covering layer is provided, the thickness by weight (i.e., gravimetric thickness) thereof, in terms of the sum of the conductive particle layer and the covering layer, is preferably up to 5.0 μm , still preferably 0.1 to 3.0 μm , yet still preferably 0.2 to 3.0 μm , and optimally 0.5 to 1.0 μm . In the present invention, the covering layer may be provided on the surface of the organic release layer not electrodeposited with the conductive particle layer.

[0040] As shown in Comparative Example 1, an initial layer of copper which directly formed on the release layer cannot be too thick, since it will interfere with laser drilling. The thickness by weight (i.e., gravimetric thickness) of such covering layer should be, in terms of the sum of the conductive particle layer and the covering layer, up to 5.0 μm , preferably 0.1 to 3.0 μm , more preferably 0.2 to 3.0 μm , and especially 0.5 to 1.0 μm .

[0041] Although the covering layer is not particularly limited, it is preferred that, like the conductive particle layer, the covering plating layer be composed of copper or a copper foil.

[0042] When the covering layer is provided on the surface of the conductive particle layer, the adhesion between the conductive particle layer and the organic release layer can be enhanced. Accordingly, in the bonding of a copper-clad laminate to a base material resin, the conductive fine particles are not buried in the base material resin by the heat and pressure applied in producing printed wiring boards.

[0043] Although the covering layer is preferred, it may be omitted, as in Example 2, when the shape and size of the deposited particles are such that they are not buried in the base resin during lamination.

[0044] In the composite foil of the present invention, the conductive particle layer 24 has a surface whose surface roughness (Rz) is preferably in the range of 0.5 to 10.0 μm , still preferably 1.0 to 5.0 μm , and yet still preferably 2.0 to 4.5 μm . The surface roughness recited in the present invention is measured in accordance with the method of JIS C 6515 or IPC TM 650.

[0045] Further, in the composite foil of the present invention, it is preferred that the peel strength from carrier as measured in accordance with JIS C 6481 be in the range of 1 to 200 gf/cm, practically 5 to 100 gf/cm. When the peel strength of the composite foil falls within the above range, the conductive carrier can easily be removed from the composite foil after forming a copper-clad laminate from the composite foil of the present invention.

[0046] In the composite foil of the present invention, combinations of conductive fine particles and conductive carrier are, for example, as specified in the following Table 1.

Table 1

| conductive fine particles | carrier |
|-----------------------------|---|
| copper fine particles | copper foil |
| copper fine particles | copper alloy foil |
| copper fine particles | aluminum foil |
| copper fine particles | aluminum foil having its surface plated with copper |
| copper alloy fine particles | copper foil |
| copper alloy fine particles | copper alloy foil |
| copper alloy fine particles | aluminum foil having its surface plated with copper |

[0047] The production of a copper-clad laminate from the above composite foil of the present invention enables:

laser perforating the copper-clad laminate without the occurrence of burrs even at lower laser output, and

obtaining a printed wiring board or multilayer printed wiring board having fine-pitch patterns.

Process for producing composite foil

[0048] The process for producing a composite foil according to the present invention will now be described.

[0049] In the present invention, first, an organic release layer is formed on a surface of a conductive carrier.

[0050] The conductive carrier for use in the process is the same as described hereinbefore. It is preferably composed of copper or a copper foil.

[0051] Prior to the formation of the organic release layer, the carrier surface may be washed with a pickling liquid or water in order to remove oxide films therefrom.

[0052] The organic release layer, as aforementioned, is preferably composed of at least one organic compound selected from the group consisting of the nitrogen-containing compounds, the sulfur-containing compounds and the carboxylic acids. Although the method of forming the organic release layer on the carrier surface is not particularly limited as long as a uniform organic release layer can be formed on the carrier surface, the organic release layer is generally formed by the immersion method or coating method. For example, when the organic release layer is formed by the immersion method, the carrier is immersed in an aqueous solution of an organic compound capable of forming an organic release layer, such as triazole, to thereby form the organic release layer. The concentration of organic compound in the aqueous solution may be, for example, in the range of 0.01 to 1% by weight and the immersion time may be in the range of 5 to 60 sec. Although the effects of the present invention are not lessened by an increase of concentration or a prolongation of immersion time, falling within the above ranges is preferred from the viewpoint of cost and productivity.

[0053] The organic release layer superimposed on the conductive carrier is electrodeposited with conductive fine particles by performing cathodic electrolysis in a plating bath. The current density applied is appropriately selected depending on the composition of the bath. For example, cathodic electrolysis is performed at a current density of 1 to 50 A/dm². The plating bath is, for example, selected from among a copper pyrophosphate plating bath, a copper cyanide plating bath and an acid copper sulfate plating bath. An acid copper sulfate plating bath and a copper pyrophosphate plating bath are preferred.

[0054] When the electrodeposition of conductive particles is performed with the use of an acid copper sulfate plating bath, preferred use is made of a copper sulfate plating bath whose copper concentration is in the range of 5 to 60 g/liter and whose sulfuric acid concentration is in the range of 50 to 200 g/liter. The plating period is preferably in the range of 5 to 20 sec. Although the conductive particles can be obtained even if the copper concentration falls outside the above range, the copper concentration of less than 5 g/liter may cause uneven electroplating and the copper concentration of greater than 60 g/liter necessitates an increase of current density which may be economically disadvantageous. It is generally preferred that the temperature of the plating bath be in the range of 15 to 40°C.

[0055] The above copper sulfate plating bath, may be doped as desired with 0.5 to 20 ppm of one or more additives, such as α -naphthoquinoline, dextrin, glue, PVA, triethanolamine and thiourea. The additives enable controlling the configuration of the primary particles constituting the conductive particle layer.

[0056] In the present invention, after the formation of an electrodeposit on the organic release layer coated on the conductive carrier, the electrodeposit may further be electroplated so that the conductive particles are plated with a covering layer.

[0057] Although the electroplating conditions are not particularly limited, it is generally preferred to employ a copper sulfate plating bath whose copper concentration, sulfuric acid concentration and temperature are 50 to 80 g/liter, 50 to 150 g/liter and 40 to 50°C, respectively. The current density is preferably in the range of 10 to 50 A/dm², and the plating period is preferably in the range of 5 to 60 sec.

[0058] The covering layer can also be formed by the use of a copper pyrophosphate plating bath. Although the plating conditions therefor are not particularly limited, it is preferred that the copper concentration and the potassium pyrophosphate concentration be in the range of 10 to 50 g/liter and 100 to 700 g/liter, respectively. The pH of the electrolytic solution is preferably in the range of 8 to 12, and the current density is preferably in the range of 3 to 15 A/dm². The plating period is preferably in the range of 3 to 80 sec. With respect to the covering layer, the organic release layer surface not electrodeposited with the conductive particles may be furnished with the covering plating layer.

[0059] When the covering layer is provided, in the bonding of a copper-clad laminate to a base material resin, conductive fine particles are not buried in the base material resin by the heat and pressure applied in producing the printed wiring board.

[0060] The thus obtained composite foil may further be passivated. The passivation can be performed by conventional methods such as those using zinc and a chromate.

Laminate and printed wiring board

[0061] The copper-clad laminate of the present invention can be produced from the above composite foil of the present invention.

[0062] Specifically, the copper-clad laminate can be obtained by bonding the above composite foil to an insulating resin substrate so that the conductive particle layer faces the insulating resin substrate.

[0063] The insulating resin substrate is not particularly limited and is, for example, composed of composite base materials such as glass epoxy, glass polyimide, glass polyester, aramide epoxy, FR-4, paper-phenolic and paper-epoxy base materials.

[0064] It is preferred that the bonding of the composite foil to the insulating resin substrate be performed at 155 to 230°C under a pressure of 15 to 150 kgf/cm².

[0065] In the laminate of the present invention, only the conductive carrier may be removed therefrom after the bonding of the conductive particulate mass of the composite foil to the insulating resin substrate.

[0066] In the present invention, a structure obtained by bonding the composite foil to the insulating resin substrate may be referred to as "laminate with carrier", and a structure obtained by removing the conductive carrier from the laminate with carrier may be referred to as "laminate without carrier".

[0067] When the conductive carrier is stripped from the copper-clad laminate, the copper-clad laminate without carrier consists of the conductive particle layer and the insulating resin substrate (in the present invention, this copper-clad laminate without carrier may be simply referred to as "copper-clad laminate") can be obtained. With respect to the thus obtained copper-clad laminate without carrier, when laser perforating for producing via holes is performed to the conductive particle layer, burrs do not occur at the via hole edges, in contrast to the conventional copper-clad laminates whose outer layers are composed of copper foils. Moreover, the perforating of the conductive particle layer can be accomplished with the use of laser of lower output than that used to perforate conventional copper foil.

[0068] The method of producing the printed wiring board of the present invention is not particularly limited. For example, the panel plating process or the pattern plating process can be employed.

[0069] In the panel plating process, the printed wiring board is produced through, for example, the process shown in Fig. 2. Specifically, referring first to Fig. 2(a), the above composite foil is bonded to an insulating resin substrate 31, and only the conductive carrier is removed to obtain a laminate having its surface furnished with the conductive particle layers 24. Subsequently, referring to Fig. 2(b), the laminate having its surface furnished with the conductive particle layers 24 is perforated by laser irradiation to thereby form a via hole 32. The surface defining the via hole 32 and the surface of the insulating resin layer 31 are subjected to electroless plating and electroplating to thereby form plating layers 33 as shown in Fig. 2(c). Referring now to Fig. 2(d), resists 34 are provided on surfaces of the plating layers 33 so as to have patterns corresponding to wiring patterns to be formed. Referring to Fig. 2(e), at portions on which no wiring patterns to be formed, the plating layers 33 are removed by etching. Thereafter, the resists are removed to thereby obtain wiring patterns 33, as shown in Fig. 2(f).

[0070] In the printed wiring board of the present invention, the conductive fine particles of the composite foil are provided on a surface of insulating resin layer to thereby enable forming thin fine-pitch wiring patterns. Further, the conductive particle layer as a composite material for forming electrical wiring patterns is on the surface of insulating resin layer, so that, in contrast to the use of metal foils, burrs do not occur at the via hole edges during laser perforating to thereby avoid the problems of burr enlargement at the via hole edges, poor adherence of resists and insufficient etching. Therefore, it is not needed to remove portions to be perforated by etching, etc. in advance. Moreover, the laser perforating can be performed with the use of laser of lower output than that of laser heretofore proposed.

[0071] On the other hand, in the pattern plating process, the printed wiring board is produced through, for example, the process of Fig. 3. Specifically, a laminate comprising conductive particle layer 24 on surfaces of insulating resin layer 41 as shown in Fig. 3(a) is perforated by direct laser irradiation, thereby forming via holes 42 as shown in Fig. 3(b). Referring now to Fig. 3(c), electroless plating is applied to all the surfaces including via hole insides to thereby form plating layer 43. Then, referring to Fig. 3(d), resists 44 are provided so as to have patterns corresponding to gaps or spaces of the wiring patterns to be formed. Subsequently, referring to Fig. 3(e), circuits 45 of given thickness are formed by electroplating. Thereafter, the surface resists 44 are removed, and all the surfaces are etched so that especially the electroless plating layers 43 and conductive particle layer 24 at noncircuit portions are removed (this may be referred to as "flash etching") to thereby obtain circuits 45 as shown in Fig. 3(f). In this process, solder plating of tin, lead or the like may be applied to surfaces of the wiring patterns prior to resist removal.

[0072] Both the above panel plating process and pattern plating process enable producing a printed wiring board which is excellent in the adherence of the wiring pattern layers to insulating resin and also in heat resistance.

[0073] Furthermore, in the present invention, a multilayer printed wiring board can be obtained by providing the above printed wiring board for use as a substrate with inner layer circuits; bonding it, through an insulating resin layer, with the composite foil of the present invention; removing the conductive carrier; disposing the conductive particle layer on the surface of insulating resin layer; forming via holes and wiring patterns; and plating.

[0074] The number of layers of the multilayer printed wiring board can be increased by repeating this operation.

[0075] The formation of a copper-clad laminate from the composite foil of the present invention enables one to dispose an extremely thin conductive particle layer on a surface of insulating resin layer, so that the problem of burrs occurring at the perforating of copper-clad laminate by insulating resin layer drilling, laser perforating or similar means can be solved. Further, the laser perforating can be performed with the use of laser of lower output than that of lasers heretofore proposed. Still further, the use of the above composite foil of the present invention enables reducing the thickness of wiring patterns, so that printed wiring boards having fine-pitch wiring patterns can be produced with high efficiency. Still further, the use of the above composite foil of the present invention enables realizing satisfactory bonding strength between the insulating resin layer and the wiring patterns.

[0076] The present invention will now be illustrated in greater detail with reference to the following Examples, which in no way limit the scope of the invention.

EXAMPLE 1

[0077] Electrolytic copper foil of 35 μm thickness was provided as a conductive carrier, and its shiny side was immersed in a 10 g/liter aqueous solution of carboxybenzotriazole at 40°C for 30 sec so that an organic release layer was formed on the surface of the carrier copper foil.

[0078] Subsequently, the surface of the thus formed organic release layer was washed with water and subjected to cathodic electrolysis. The cathodic electrolysis was performed at a current density of 15 A/dm² for 7 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 30°C, 10 g/liter and 170 g/liter, respectively. Thus, a conductive particle layer was formed on the organic release layer.

[0079] The composite foil thus furnished with the conductive particle layer was subjected to further cathodic electrolysis. The cathodic electrolysis was performed at a current density of 30 A/dm² for 10 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 50°C, 75 g/liter and 80 g/liter, respectively. Thus, a covering layer was plated on the electrodeposit, and there was obtained a nodular conductive particle layer. The thickness of the resultant conductive particle layer after the formation of the covering layer was about 1.5 μm .

[0080] The thus obtained composite foil was further passivated with zinc and a chromate.

EXAMPLE 2

[0081] Electrolytic copper foil of 35 μm thickness was provided as a conductive carrier, and its shiny side was immersed in a 10 g/liter aqueous solution of carboxybenzotriazole at 40°C for 30 sec so that an organic release layer was formed on the surface of the carrier copper foil.

[0082] Subsequently, the surface of the thus formed organic release layer was washed with water and subjected to cathodic electrolysis. The cathodic electrolysis was performed at a current density of 15 A/dm² for 7 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 20°C, 17 g/liter and 120 g/liter, respectively, which plating bath further contained 4 ppm of α -naphthoquinoline. Thus, a whiskery conductive particle layer (size: 0.6 μm) was formed.

[0083] After the electrodeposition of conductive particle layer, passivation was performed under the same conditions as in Example 1. Thus, a composite foil was obtained.

EXAMPLE 3

[0084] Electrolytic copper foil of 35 μm thickness was provided as a conductive carrier, and its shiny side was immersed in a 10 g/liter aqueous solution of carboxybenzotriazole at 40°C for 30 sec so that an organic release layer was formed on the surface of the carrier copper foil.

[0085] Subsequently, the surface of the thus formed organic release layer was washed with water and subjected to cathodic electrolysis. The cathodic electrolysis was performed at a current density of 20 A/dm² for 5 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 40°C, 20 g/liter and 70 g/liter, respectively. Thus, a conductive particle layer was formed on the organic release layer.

[0086] The composite foil thus furnished with the electrodeposit was subjected to further cathodic electrolysis. The cathodic electrolysis was performed at a current density of 30 A/dm² for 10 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were, 50°C, 75 g/liter and 80 g/liter, respectively. Thus, a covering layer was plated on the conductive particle layer, and there was obtained a fringed conductive particle layer. The size of the resultant conductive particle layer after the plating the covering layer was about 2.3 μm .

[0087] The thus obtained composite foil was further passivated in the same manner as in Example 1.

EXAMPLES 4 TO 6

[0088] 0.1 mm thick BT resin prepreg (produced by MITSUBISHI GAS CHEMICAL CO., INC.) was laminated with each of the composite foils obtained in Examples 1 to 3 so that the conductive particle layer of the composite foil was joined to the BT resin prepreg and pressed at 200°C under 25 kgf/cm² for 35 min to thereby effect a lamination. With respect to each of the obtained laminates, the peel strength exerted at stripping of the copper foil as the conductive carrier from the laminate was measured in accordance with JIS C 6481. All the peel strengths exhibited by the laminates were in the range of 9 to 12 gf/cm.

[0089] Therefore, it was found that the composite foils obtained in Examples 1 to 3 permitted easily stripping the copper foil therefrom but exhibited appropriate bonding strengths.

[0090] Thereafter, all the laminate surface was subjected to copper electroless plating and electroplating so that a copper layer of 18 µm total thickness was formed on the laminate surface. The resultant laminates were etched so that wiring patterns of line width/line spacing = 30 µm /30 µm were formed. Etchability was satisfactory, and it was found that the obtained composite material foils were very excellent in respect of minute circuit formation.

EXAMPLES 7 TO 9

[0091] Both sides of 0.5 mm thick FR-4 base material (R-1766 produced by Matsushita Electric Works, Ltd.) were furnished with internal-layer circuits and subjected to black oxide treatment. Composite foils produced in each of Examples 1 to 3 were stacked with the resultant FR-4 base material interposed therebetween so that the conductive particle layers were joined to both 18 µm thick internal-layer circuit surfaces of the base material, and pressed at 180°C under 20 kgf/cm² for 60 min by means of a vacuum press. The carrier copper foils were stripped off, and thus there were obtained 4-layer boards with internal-layer wiring patterns.

[0092] Via holes were formed at given positions of the conductive particle layers of each of the thus obtained 4-layer boards with internal-layer wiring patterns by carrying out 4-shot irradiation of carbon dioxide gas laser (ML505DT manufactured by Mitsubishi Electric Corp.) under conditions such that the beam diameter, current and pulse width were 220 µm, 12 A and 50 µsec, respectively.

[0093] With respect to all the 4-layer boards with internal-layer wiring patterns, burrs and other defects were not observed at the outer-layer wiring patterns of the via hole irradiation portions.

COMPARATIVE EXAMPLE 1

[0094] Electrolytic copper foil of 35 µm thickness was provided as a conductive carrier, and its shiny side was immersed in a 10 g/liter aqueous solution of carboxybenzotriazole at 40°C for 30 sec so that an organic release layer was formed on the surface of the carrier copper foil.

[0095] Subsequently, the surface of the thus formed organic release layer was washed with water and subjected to cathodic electrolysis. The cathodic electrolysis was performed at a current density of 30 A/dm² for 50 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 50°C, 80 g/liter and 70 g/liter, respectively. Thus, an ultra-thin copper foil of about 6 µm thickness was formed on the organic release layer.

[0096] The surface of the thus formed ultra-thin copper foil was washed with water and subjected to cathodic electrolysis for surface roughening. The cathodic electrolysis was performed at a current density of 15 A/dm² for 7 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 30°C, 10 g/liter and 170 g/liter, respectively. Thus, a conductive particle layer was formed on the ultra-thin copper foil. Further cathodic electrolysis was performed at a current density of 30 A/dm² for 10 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 50°C, 75 g/liter and 80 g/liter, respectively. Thus, a covering plating layer was formed on the conductive particle layer.

[0097] The thus obtained composite foil was further passivated with zinc and a chromate. 0.1 mm thick BT resin prepreg (produced by MITSUBISHI GAS CHEMICAL CO., INC.) was laminated with the passivated composite foil so that the ultra-thin copper foil furnished with the conductive particle layer was joined to the BT resin prepreg and pressed at 200°C under 25 kgf/cm² for 35 min to thereby obtain a laminate. With respect to this laminate, the peel strength exerted at stripping of the copper foil as the conductive carrier from the laminate was measured in accordance with JIS C 6481. The peel strength was 10 gf/cm. Therefore, it was found that the obtained composite foil permitted easily stripping the carrier copper foil from the laminate but exhibited appropriate bonding strength.

[0098] 4-layer board with internal-layer circuits was produced from this composite foil in the same manner as in Examples 7 to 9. Via holes were formed at given positions of the ultra-thin copper foil surface of an outer layer of the thus obtained 4-layer board with internal-layer wiring patterns by carrying out 4-shot irradiation of carbon dioxide gas laser (ML505DT manufactured by Mitsubishi Electric Corp.) under conditions such that the beam diameter, current and pulse width were 220 µm, 12 A and 50 µsec, respectively. Copper burrs were observed at the outer-layer wiring patterns

of the via hole irradiation portions.

EXAMPLE 10

[0099] Electrolytic copper foil of 35 μm thickness was provided as a conductive carrier, and its shiny side was washed with a washing liquid containing 100 g/liter of sulfuric acid for 30 sec. After the sulfuric acid washing, the copper foil was washed with purified water. The shiny side of the washed copper foil was immersed in a 5 g/liter aqueous solution of triazinethiol at 40°C for 30 sec so that an organic release layer was formed on the surface of the carrier copper foil.

[0100] Subsequently, the surface of the thus formed organic release layer was washed with water and subjected to cathodic electrolysis. The cathodic electrolysis was performed at a current density of 15 A/dm² for 8 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 30°C, 10 g/liter and 170 g/liter, respectively. Thus, a conductive particulate mass of copper was electrodeposited on the surface of the organic release layer. Further cathodic electrolysis was performed at a current density of 30 A/dm² for 30 sec in a plating bath whose temperature, copper concentration and sulfuric acid concentration were 50°C, 75 g/liter and 80 g/liter, respectively.

Thus, a covering plating layer of copper was plated on the conductive particle layer.

[0101] The thus obtained composite material for forming electrical wiring patterns was washed with water, passivated and dried.

[0102] The thickness of the obtained conductive particle layer furnished with the covering layer was 1.0 μm measured in the direction of the thickness of the composite material for forming electrical wiring patterns.

[0103] The surface roughness (Rz) of the surface furnished with the conductive particle layer having the covering layer was 2.2 μm .

Laminate

[0104] Each of four 0.1 mm thick FR-4 prepregs was laminated with the thus obtained composite material for forming electrical wiring patterns so that the conductive particle layer plated with the covering layer faced the prepreg, and pressed at 175°C under 25 kg/cm² for 60 min to thereby obtain laminates.

[0105] The copper carrier was stripped off from each of the obtained laminates, and all the laminate surface was subjected to electroless plating of copper and further electroplating so that a copper layer of 35 μm total thickness was formed on the laminate surface. Thereafter, resist was provided so as to have patterns corresponding to the wiring patterns to be formed, followed by etching. Thus, evaluation samples having 10 μm wide wiring patterns were obtained. Peel strength was measured in accordance with JIS C 6481.

Adherence

[0106] The peel strength between wiring patterns and base material (FR-4) of the obtained evaluation sample was measured and found to be 1.35 kg/cm. Thus, the formed wiring patterns had satisfactory bonding strength with the base material.

Heat resistance

[0107] The obtained evaluation sample was floated on a 160°C solder bath for 20 sec, and the peel strength between wiring patterns and base material thereof was measured.

[0108] The peel strength was 1.35 kg/cm, namely no change was observed from that before the solder floating. Thus, it was demonstrated that the obtained evaluation sample had excellent heat resistance.

Printed wiring board

[0109] Each of four 0.1 mm thick FR-4 prepregs was laminated with the above obtained composite material for forming electrical wiring patterns so that the conductive particle layer faced the prepreg, and pressed at 175°C under 25 kg/cm² for 60 min to thereby effect bonding of the composite material for forming electrical wiring patterns and the prepreg. Thus, laminates were obtained.

[0110] The conductive carriers were removed from the laminates to thereby obtain prepregs covered with conductive particle layers. Perforating was conducted with the use of carbon dioxide gas laser. No burrs were observed at the perforated portions.

[0111] All the laminate surface was subjected to electroless plating of copper and further electroplating so that a copper layer of 18 μm total thickness was formed on the laminate surface. The resultant laminate was etched to thereby form wiring patterns of line width/line spacing = 30 μm /30 μm . Etchability was satisfactory, and hence it was found

that the produced composite material was very excellent in forming minute wiring patterns.

Claims

1. A composite foil comprising:

a conductive carrier;
an organic release layer coating a surface of the carrier; and
a conductive particle layer formed on the organic release layer.

2. The composite foil of claim 1, wherein the conductive particle layer has a surface whose surface roughness (Rz) is in the range of 0.5 to 10.0 μm .

3. The composite foil of claim 1, wherein the conductive particle layer has its surface plated with a covering layer.

4. The composite foil of any one of claims 1 to 3, wherein the conductive particle layer is composed of copper or a copper alloy.

5. The composite foil of any preceding claim, wherein the conductive particle layer has a thickness ranging from 0.1 to 5.0 μm .

6. The composite foil of any preceding claim, wherein the carrier is composed of a copper foil or a copper alloy foil.

7. The composite foil of any preceding claim, wherein the organic release layer is composed of at least one organic compound selected from the group consisting of nitrogen-containing compounds, sulfur-containing compounds and carboxylic acids.

8. The composite foil of any preceding claim, wherein the conductive particle layer is composed of fringed, nodular or whiskery conductive fine particles.

9. A process for producing a composite foil, comprising the steps of:

forming an organic release layer on a surface of a conductive carrier, and
electrodepositing a conductive particle layer on the organic release layer with the use of a plating bath through which current is passed at a density of 10 to 50 A/dm².

10. The process of claim 9, wherein the carrier is a copper foil or a copper alloy foil.

11. The process of either claim 9 or claim 10, wherein the organic release layer is composed of at least one organic compound selected from the group consisting of nitrogen-containing compounds, sulfur-containing compounds and carboxylic acids.

12. The process of any one of claims 9 to 11, wherein the conductive particle layer is composed of particles having a fringed, nodular or whiskery shape.

13. A copper-clad laminate comprising a composite foil of any of claims 1 to 8 laminated to a base material.

14. The copper-clad laminate of claim 13 from which the carrier has been removed.

15. A printed wiring board produced from a copper-clad laminate of claim 13 or 14.

Fig. 1



Fig. 2

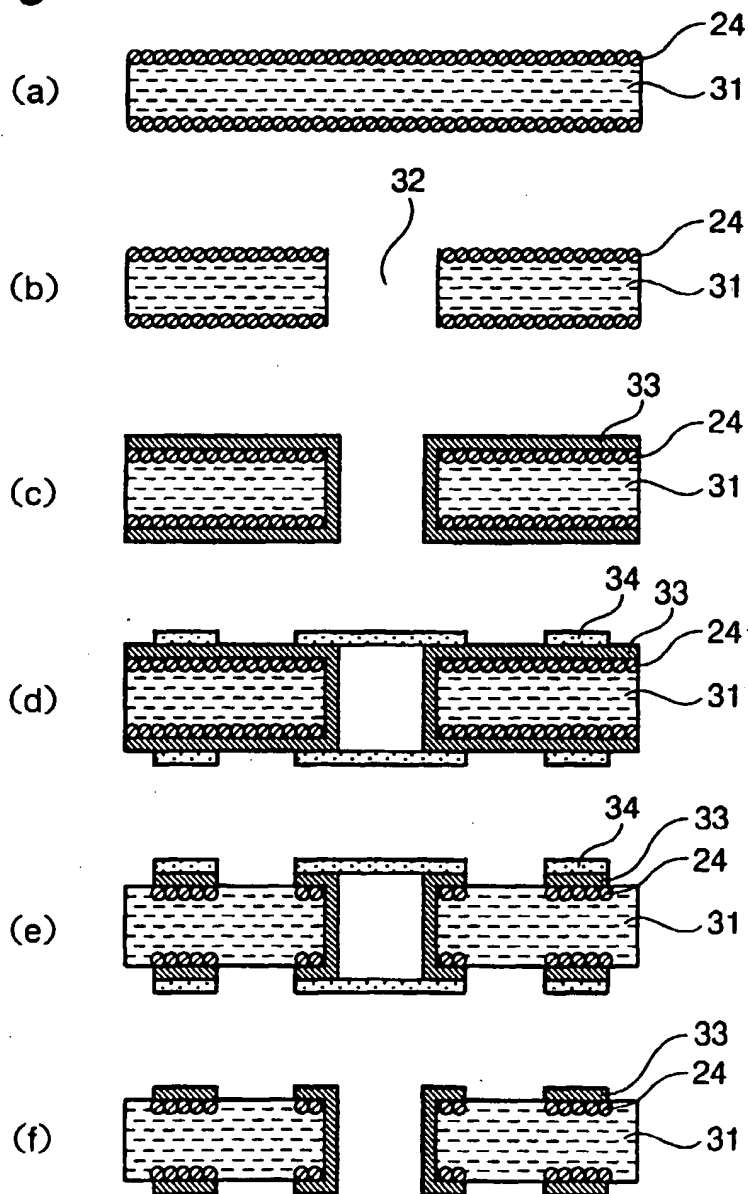


Fig. 3

